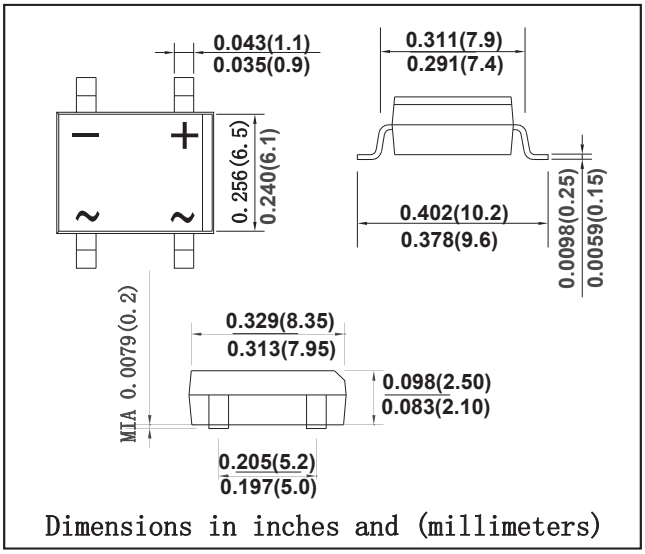
DBS SILICON BRIDGE RECTIFIERV

FEATURES

- The plastic package carries Underwrites Laboratory Flammability Classification 94V-0
- Construction utilizes void-free molded plastic technique
- High reliability
- High temperature soldering guaranteed:260 ℃/10 seconds at terminals
- Component in accordance to RoHs 2015/863 and WEEE 2012/19/EU

MECHANICAL DATA

- Case style: DBS molded plastic
- Mounting position: Any



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 ambient temperature unless otherwise specified.

Single phase,half wave,60 Hz,resistive or inductive load. For capacitive load,derate by 20%.

TYPE NUMBER	SYMBOL	DB101S	DB102S	DB103S	DB104S	DB105S	DB106S	DB107S	UNITS
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_{DC}	50	100	200	400	600	800	1000	V
RMS Reverse Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1)@T_C=100℃	IMF(AV)	1.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	45							A
I^2t Rating for Fusing (t < 8.3ms)	I^2t	8.404							A^2s
Forward Voltage per element @IF=1.0A	V_{FM}	1.0							V
Peak Reverse Current @T_A=25℃ At Rated DC Blocking Voltage @T_A=125℃	I_R	5.0 200							uA
Typical Junction Capacitance per leg	C_J	25							pF
Typical Thermal Resistance per leg	$R_{\theta JA}$ $R_{\theta JL}$	40 15							℃/W
Operating and Storage Temperature Range	T_J,T_{STG}	-55to+150							℃

RATINGS AND CHARACTERISTIC CURVES

Fig. 1 Output Current Derating Curve

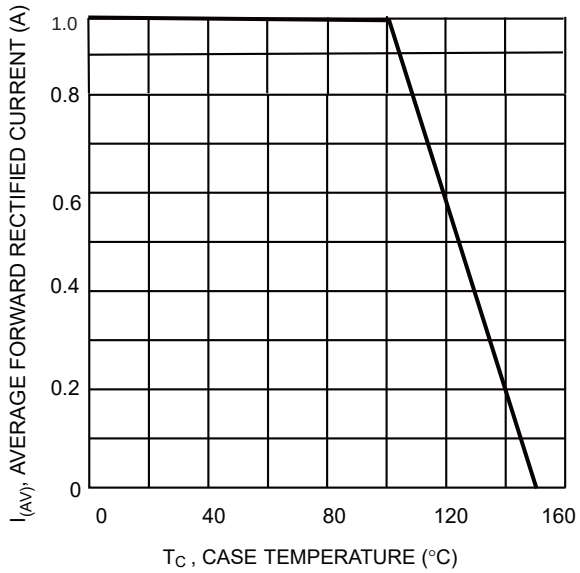


Fig. 2 Typical Forward Characteristics (per leg)

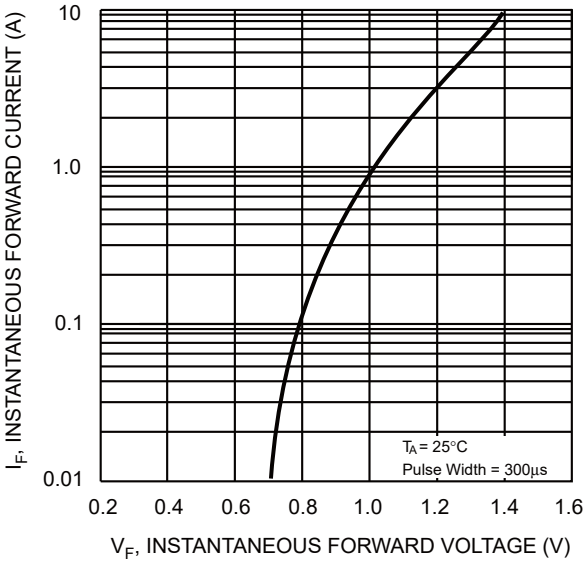


Fig. 3 Maximum Peak Forward Surge Current (per leg)

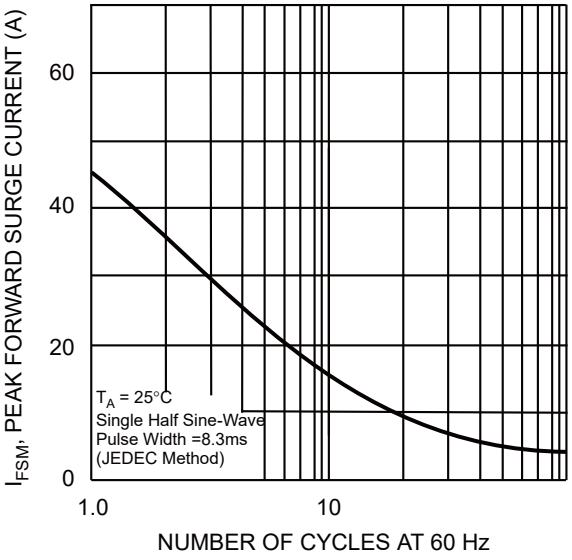


Fig. 4 Typical Reverse Characteristics (per element)

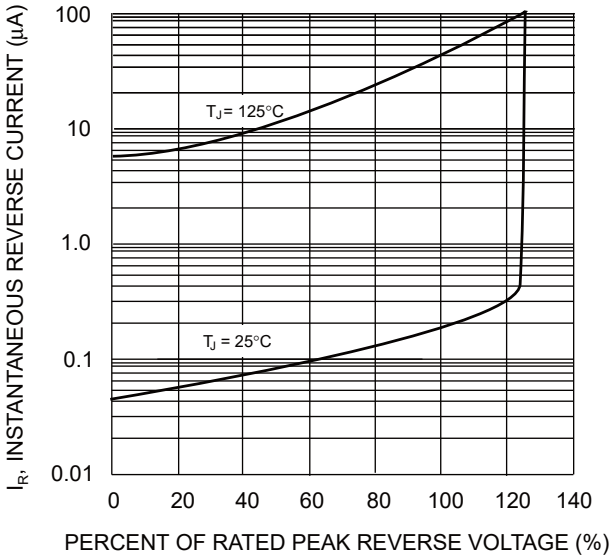


Fig. 5 **Mounting Pad Layout**